

**FORM PTO-1449**

Attorney's Docket No. (Optional)  
50N3529.01/1433

Application No. 09/661,226

Applicant

Fairman et al.

Filing Date  
September 13, 2000

Group Art Unit  
2182

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

[illegible]

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)


**EXAMINER**

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Application No. 09/661,226

Fairman et al.

Group Art Unit  
2182

U.S. PATENT DOCUMENTS

[illegible]

RECEIVED  
OCT 03 2001  
Technology Center 2100

## FOREIGN PATENT DOCUMENTS

[illegible]

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)


Comptroller

11/30/04

**Include copy of this form with next communication to applicant.**

**FORM PTO-1449**

Attorney's Docket No. (Optional)  
50N3529.01/1433

Application No. 09/661,226

**Applicant**

Fairman et al.

Filing Date

**September 13, 2000**

**Group Art Unit**

2182

## U.S. PATENT DOCUMENTS

[illegible]

## FOREIGN PATENT DOCUMENTS

	Document Number							Date	Country	Class	Subclass	Translation	
												Yes	No
tr	0	6	4	9	0	9	0	14.10.94	EPO	G06F	9/46		x
tr	0	6	9	7	6	5	1	14.08.95	EPO	G06F	0/38		x
tr	0	6	2	2	7	3	2	27.04.94	EPO	G06F	9/46		x
tr	0	2	8	5	3	1	0	22.03.88	EPO	G06F	9/46		x
tr	2	2	1	6	3	0	7	4.10.89	Great Britain	G06F	12/06		x

**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, Etc.)

49	2	Register Relocation: Flexible Contexts for Multithreading, MIT Laboratory for Computer Science, Waldspurger, C., Wehl, W., p120 - p130, 6/5/93
	✓	Register Banking For IBM System/370, IBM Technical Disclosure Bulletin, Vol.334, No.4B, p372-p373, 9/91
49	✓	A 26-Bit Microprocessor With Multi-Register Bank Architecture, Hitachi Research Laboratory, p1014 - p1019, 2/11/86
	✓	General Purpose Register Extension, IBM Technical Disclosure Bulletin, Vol.24, No.3, p1404 - p1405, 8/81.
49	✓	Shifting Register Windows, 8207 IEEE Micro, Russell, G., Shaw, P., p28 - p35, 3/93
49	✓	Real-Time Application, High Performance Systems, Goering R., p19, 22, 24, 28 - 29, 3/90

**EXAMINER**

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered.

**Include copy of this form with next communication to applicant.**